

Fig 1
(PRIOR ART)

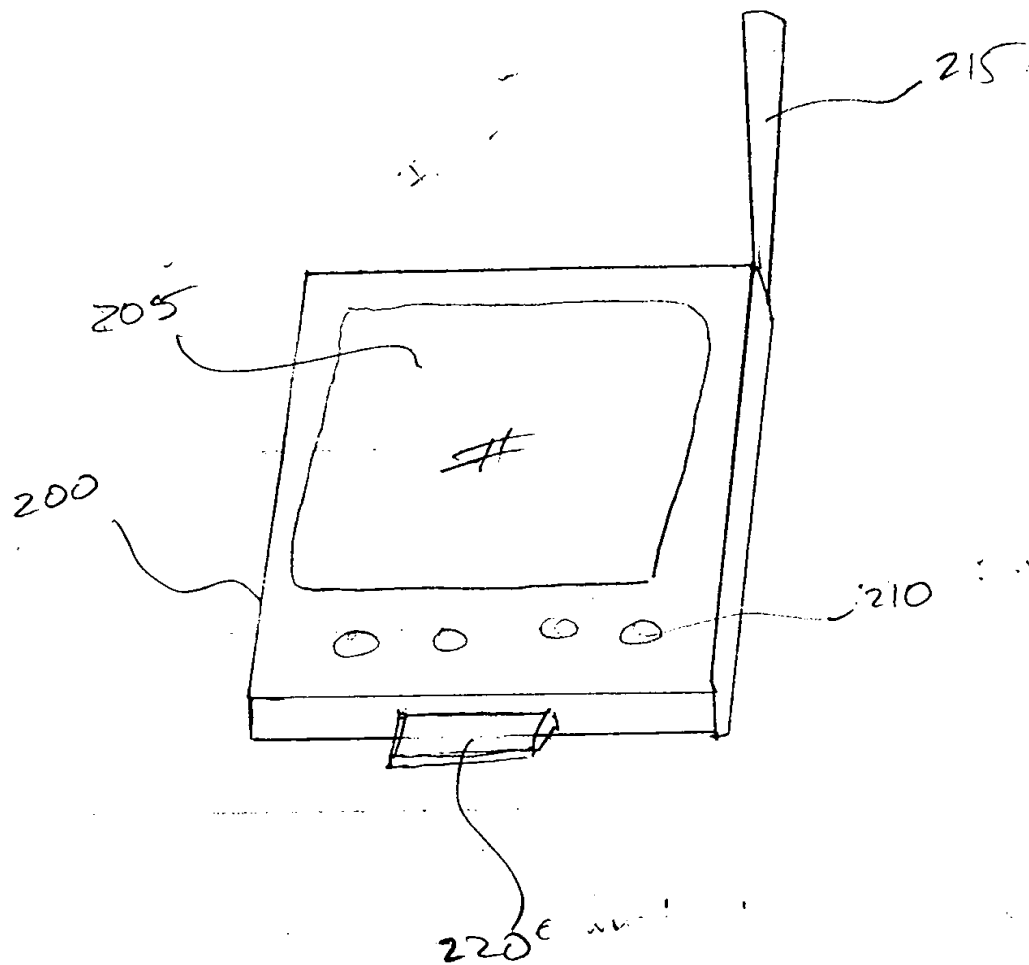


Fig 2
(PRIOR ART)

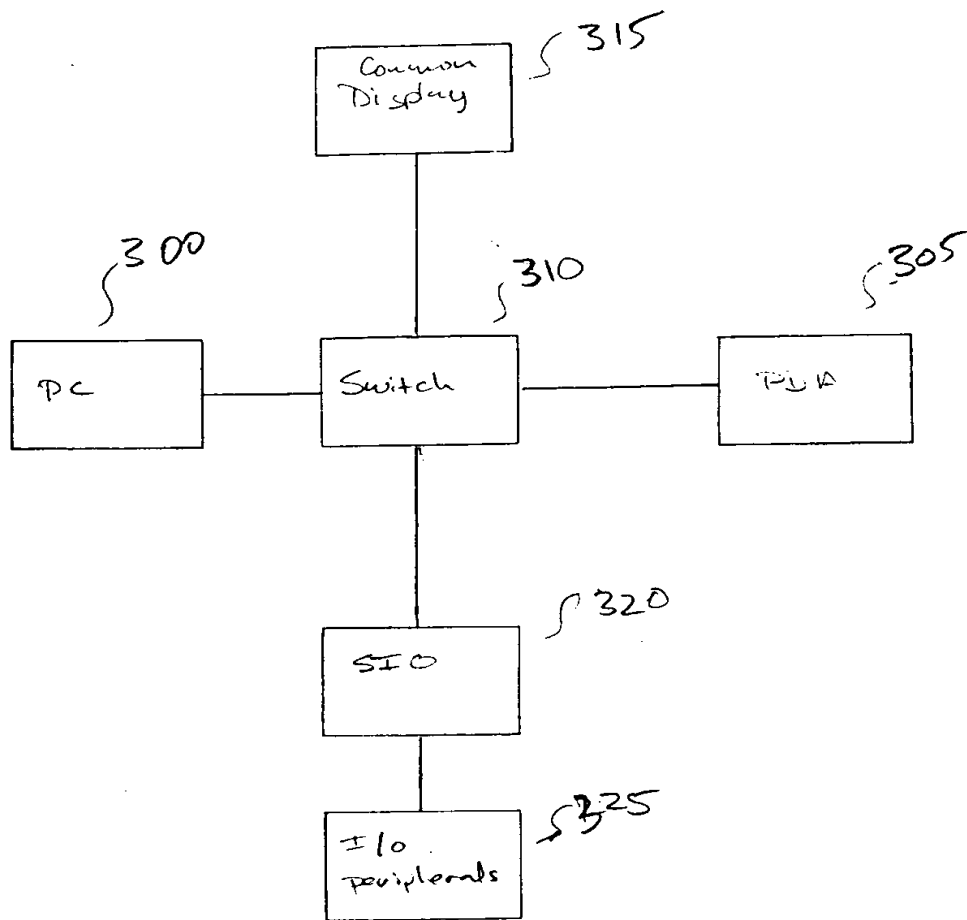


Fig. 3

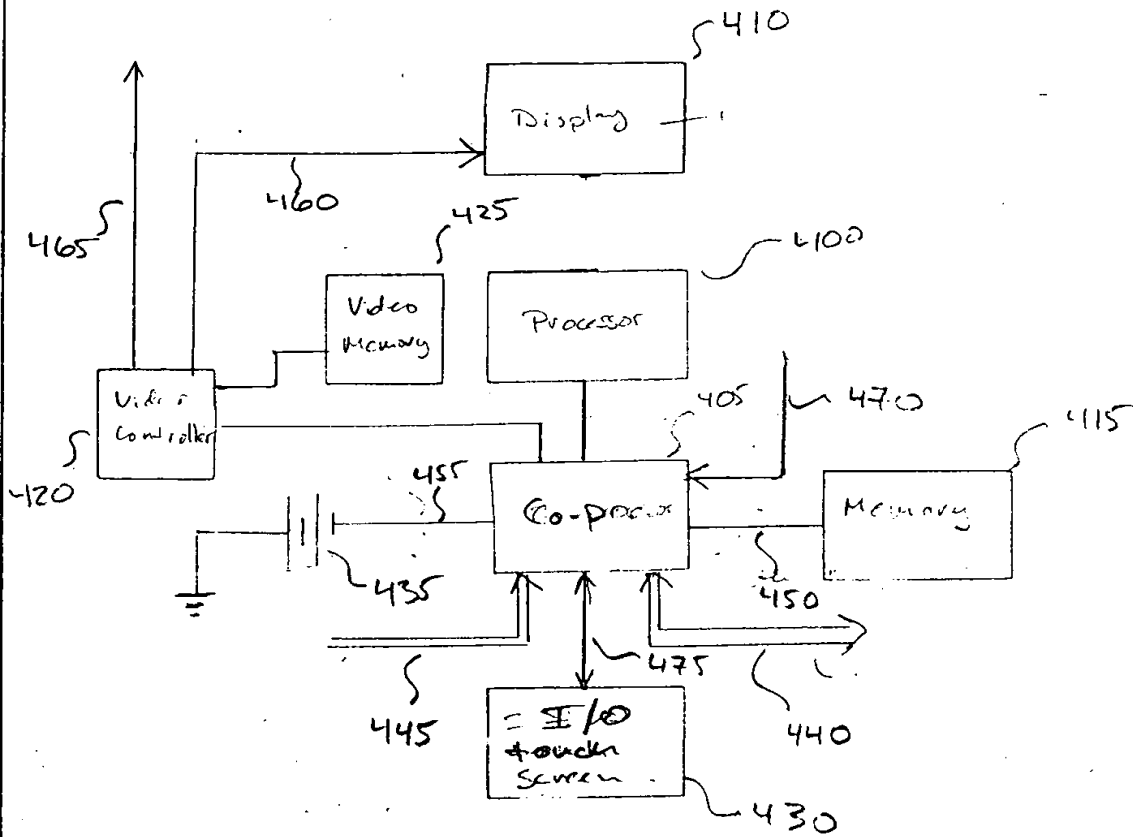


Fig. 4 A

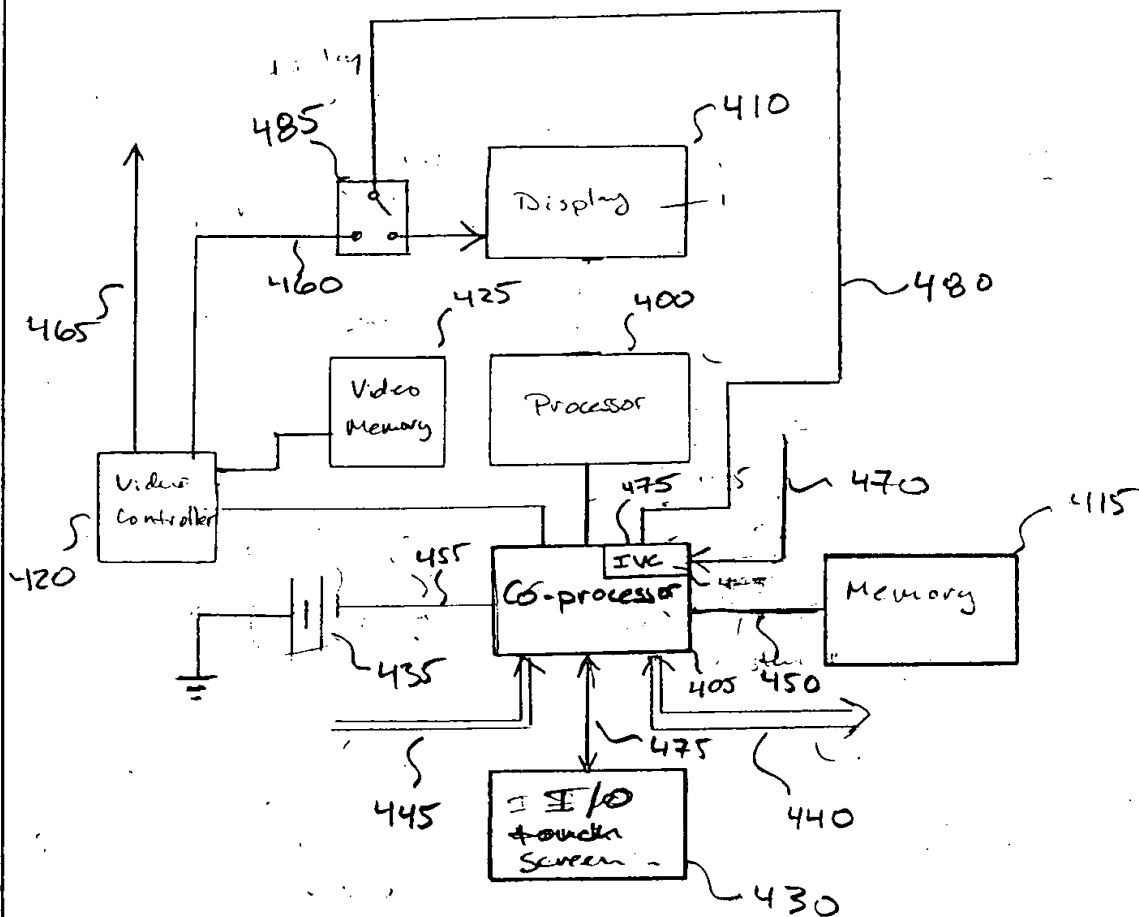


Fig. 4 B

PDA to Chassis Connection

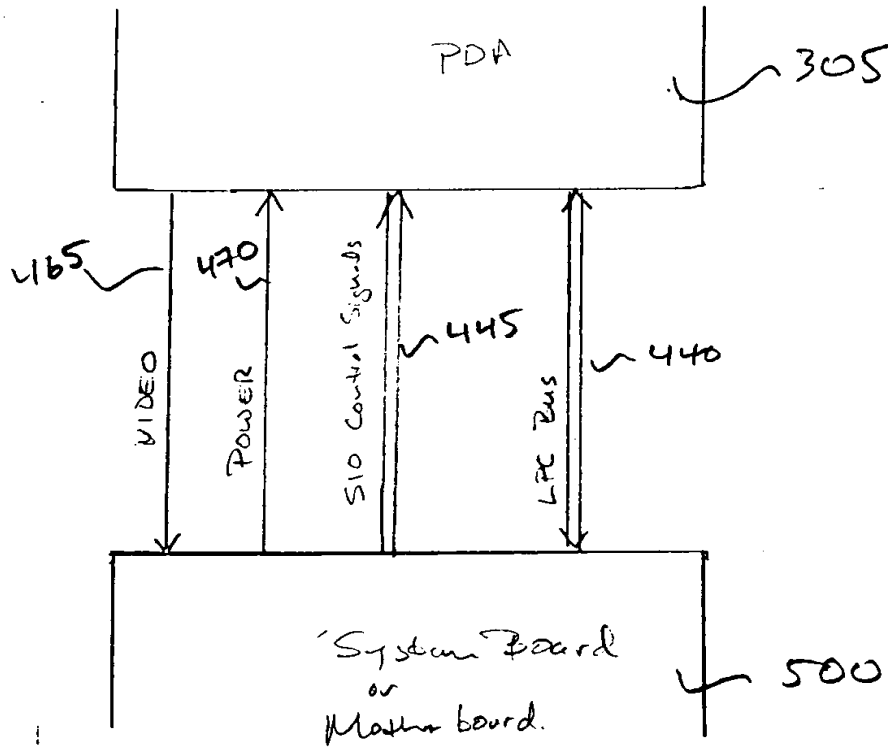


Fig 5

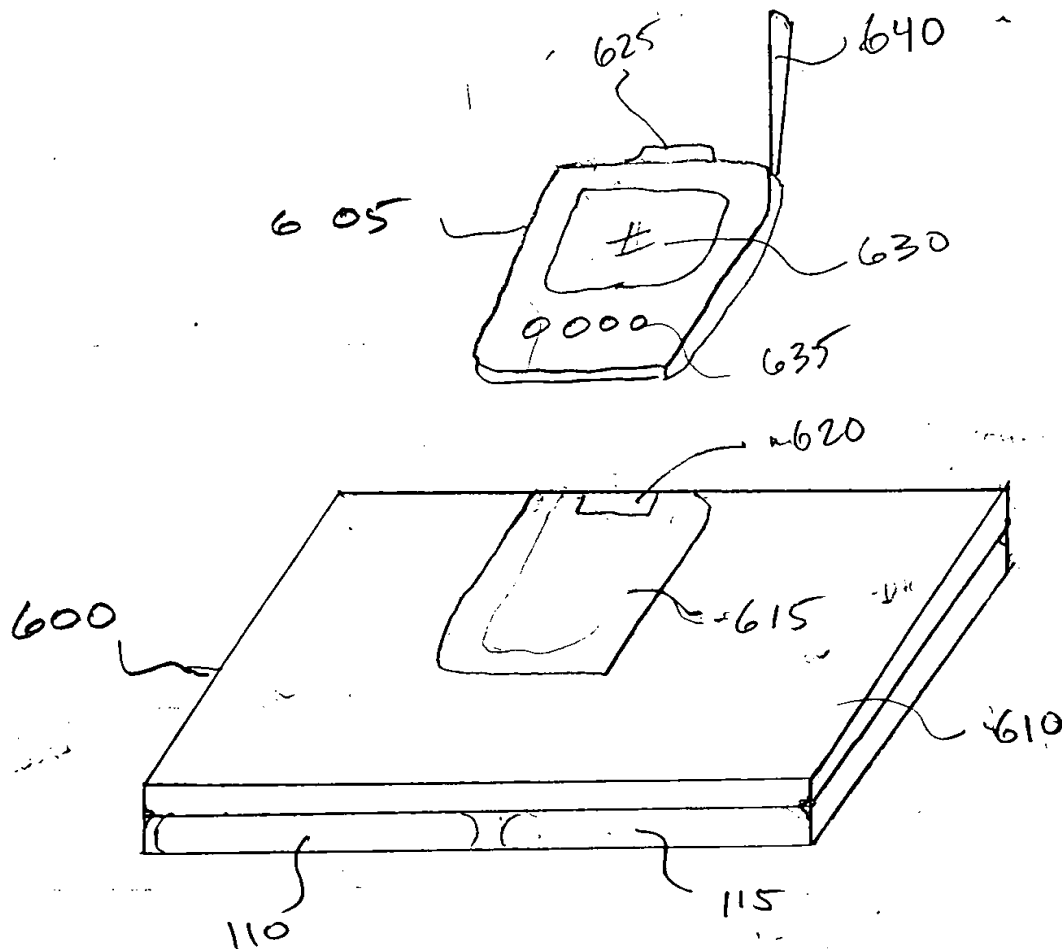


Fig. 6